

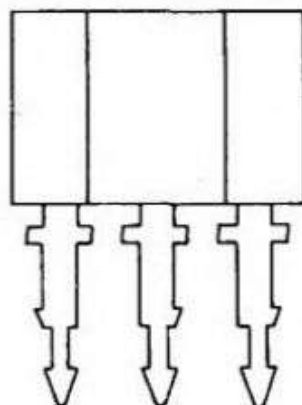
N-P-N silicon planar transistor in plastic encapsulation with three rigid self-locking strips suitable for insertion in printed circuit boards using standard grids. The transistor has a very low feedback capacitance and is primarily intended for use in the output stage of television video i.f. amplifiers.

QUICK REFERENCE DATA

V_{CEO} max.	40	V
V_{CE0} max.	25	V
I_C max.	25	mA
P_{tot} max. ($T_{amb} = 25^\circ\text{C}$)	250	mW
T_J max.	125	$^\circ\text{C}$
f_T typ. ($I_C = 5\text{mA}$, $V_{CE} = 10\text{V}$, $f = 100\text{MHz}$)	550	MHz
$-C_{re}$ typ. ($I_C = 1\text{mA}$, $V_{CE} = 10\text{V}$, $f = 10.7\text{MHz}$)	0.3	pF
G_{UM} typ. ($I_C = 7\text{mA}$, $V_{CE} = 10\text{V}$) $f = 35\text{MHz}$	43	dB
$f = 45\text{MHz}$	41	dB
Video detector output voltage, typ.	7.7	V

OUTLINE AND DIMENSIONS

For details see page 4



Front View
Scale 3:1

N.B. Devices in this Data Sheet should be ordered by the type number followed by Reference 0220.

RATINGS

Limiting values of operation according to the absolute maximum system.

Electrical

V_{CBO} max.	40	V
V_{CEO} max. (see also page 5)	25	V
V_{EBO} max.	4.0	V
I_C max.	25	mA
I_{CM} max.	25	mA
P_{tot} max. ($T_{amb} = 25^\circ\text{C}$)	250	mW

Temperature

T_{stg}	-65 to + 125	$^\circ\text{C}$
T_J max.	125	$^\circ\text{C}$

THERMAL CHARACTERISTIC

$R_{th(j-amb)}$ in free air	0.4	deg C/mW
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ELECTRICAL CHARACTERISTICS ($T_{amb} = 25^\circ\text{C}$)

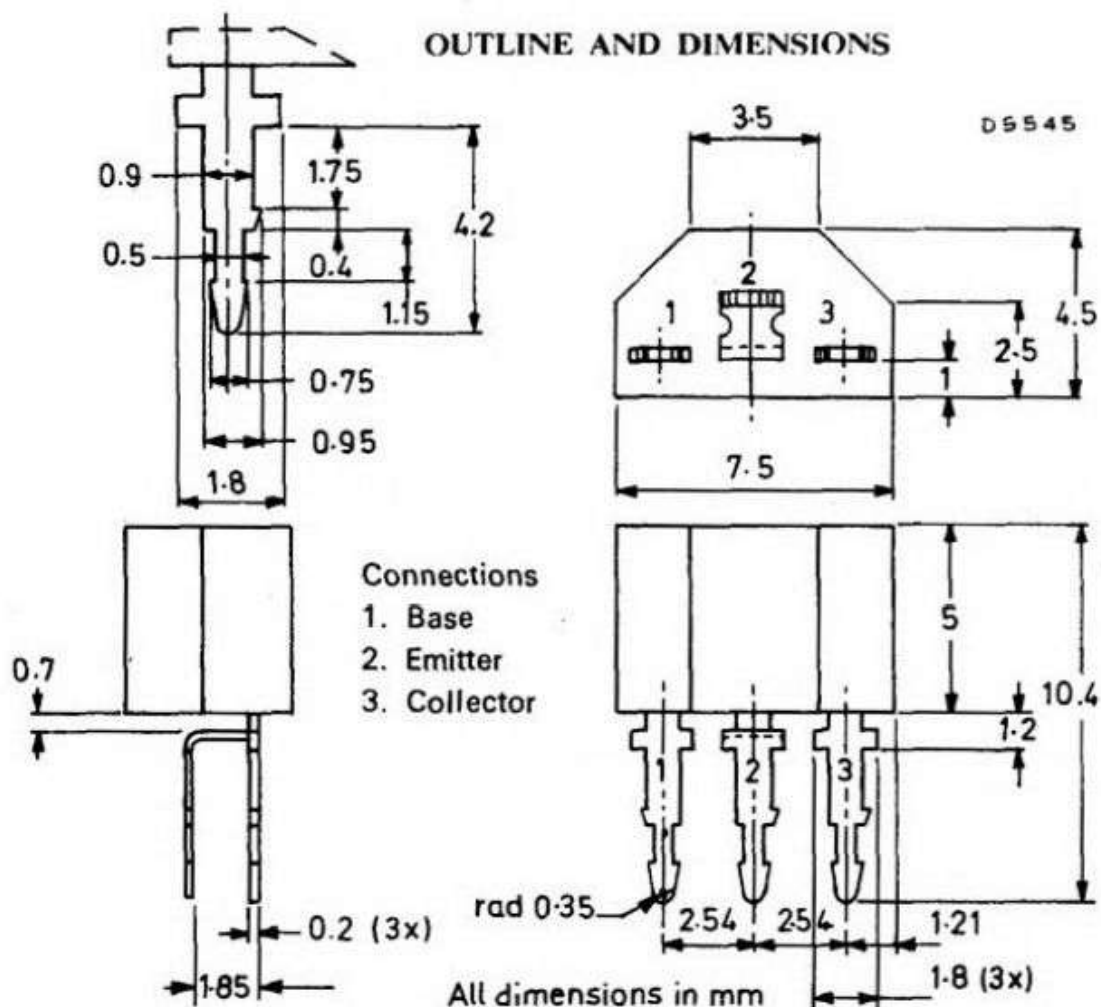
	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	
I_B Base current				
$I_C = 7.0\text{mA}, V_{CE} = 10\text{V}$	—	80	185	μA
V_{BE} *Base-emitter voltage				
$I_C = 7.0\text{mA}, V_{CE} = 10\text{V}$	—	750	900	mV
$-C_{re}$ Feedback capacitance				
$I_C = 1.0\text{mA}, V_{CE} = 10\text{V},$ $f = 10.7\text{MHz}$	—	0.3	—	pF
f_T Transition frequency				
$I_C = 5.0\text{mA}, V_{CE} = 10\text{V},$ $f = 100\text{MHz}$	—	550	—	MHz
* V_{BE}	decreases by about 1.7mV/degC with increasing temperature.			

ELECTRICAL CHARACTERISTICS (*continued*)

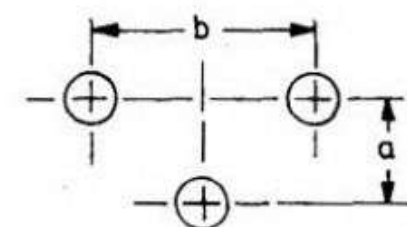
Typical y-parameters (common emitter)

$I_C = 7.0\text{mA}$, $V_{CE} = 10\text{V}$ (mounted as in the Mounting Details, 1.)

		$f = 35$	$= 45$	MHz
g_{ie}	Input conductance	4.5	5.5	mmho
C_{ie}	Input capacitance	45	45	pF
$ y_{re} $	Feedback admittance	67	86	μmho
ϕ_{re}	Phase angle of feedback admittance	268	268	deg
$ y_{fe} $	Transfer admittance	170	155	mmho
ϕ_{fe}	Phase angle of transfer admittance	338	335	deg
g_{oe}	Output conductance	85	95	μmho
C_{oe}	Output capacitance	1.8	1.8	pF
G_{UM}	Maximum unilateralised power gain			
	$G_{UM} \text{ (in dB)} = 10 \log \frac{ y_{fe} ^2}{4g_{ie}g_{oe}}$			
	$I_C = 7.0\text{mA}$, $V_{CE} = 10\text{V}$	43	41	dB

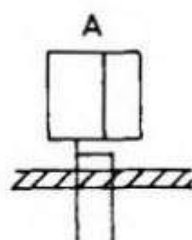


MOUNTING DETAILS

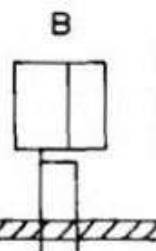


$$a = 2.49 \text{ to } 2.59 \text{ mm}$$

$$b = 5.03 \text{ to } 5.13 \text{ mm}$$



Maximum thickness of
printed board = 1.7 mm
Recommended hole diameter
1.0 to 1.1 mm
(1.0 to 1.3 mm allowable)

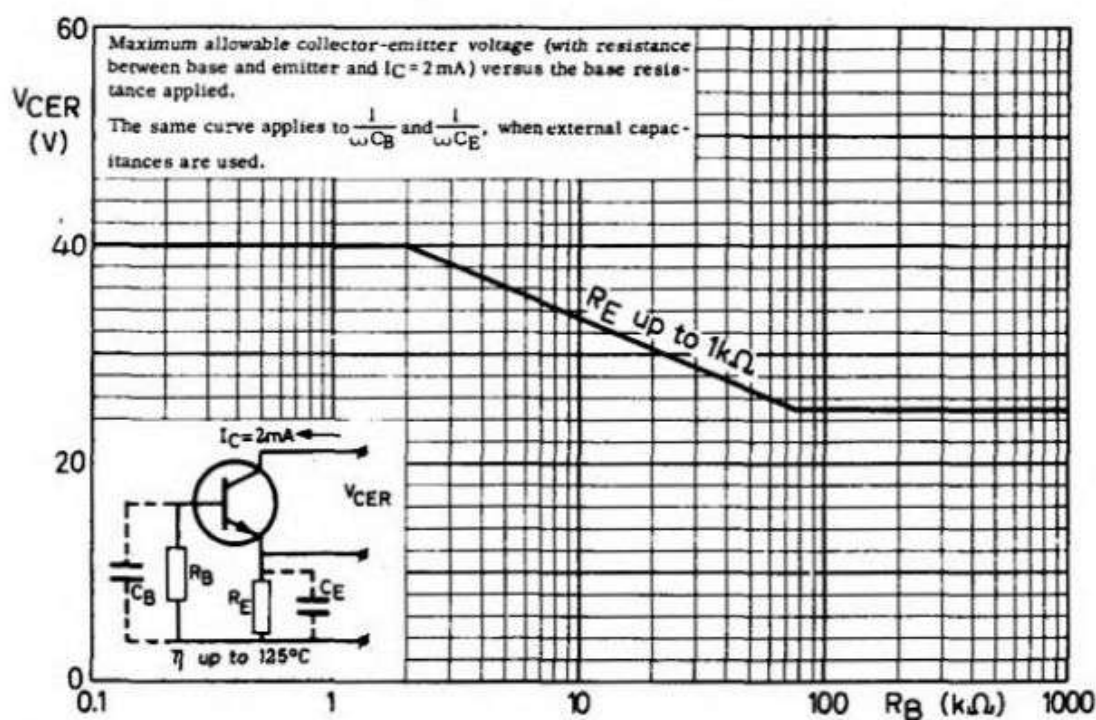
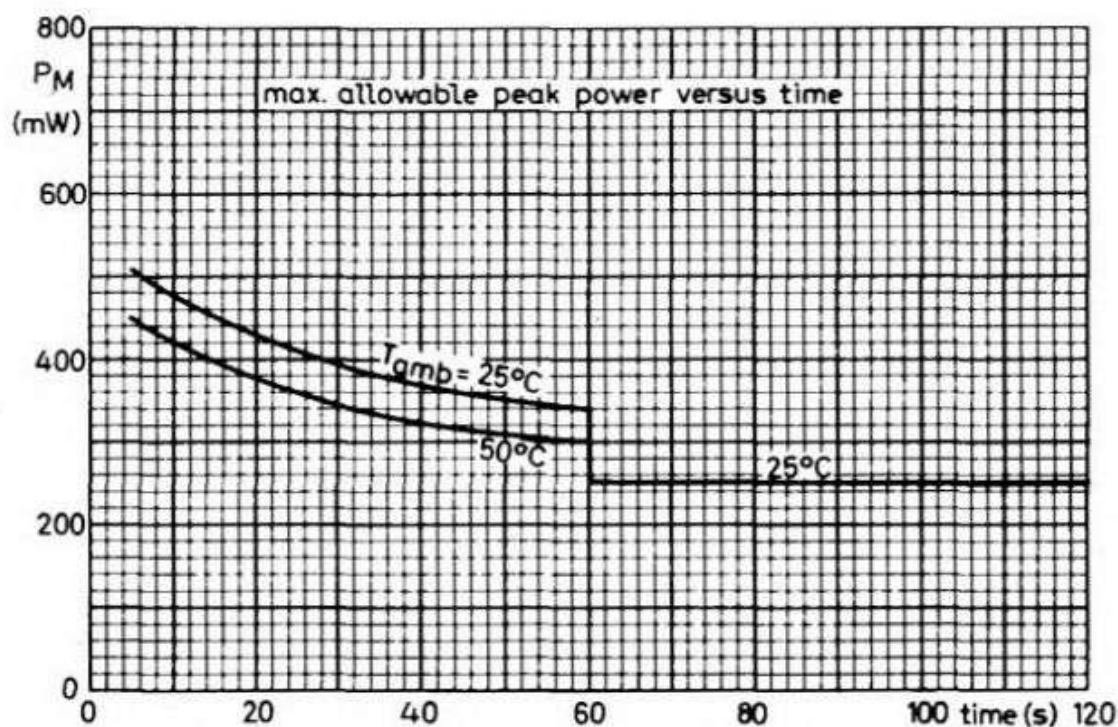


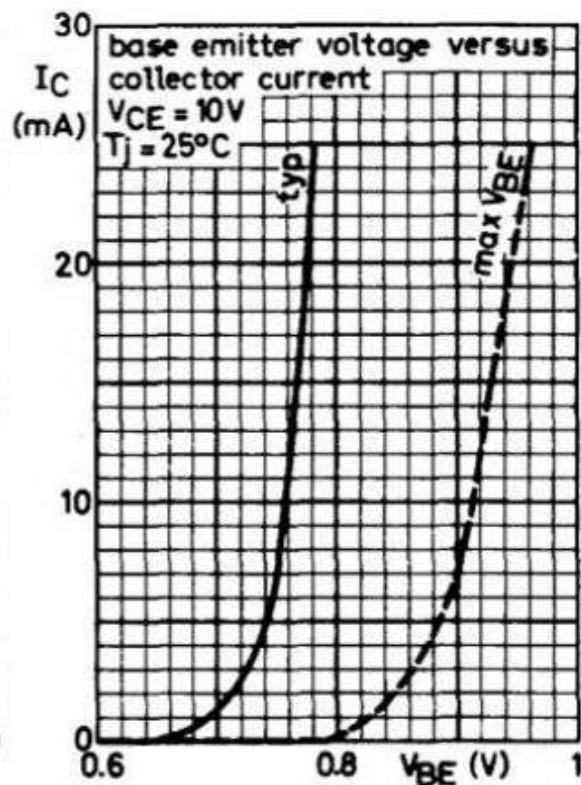
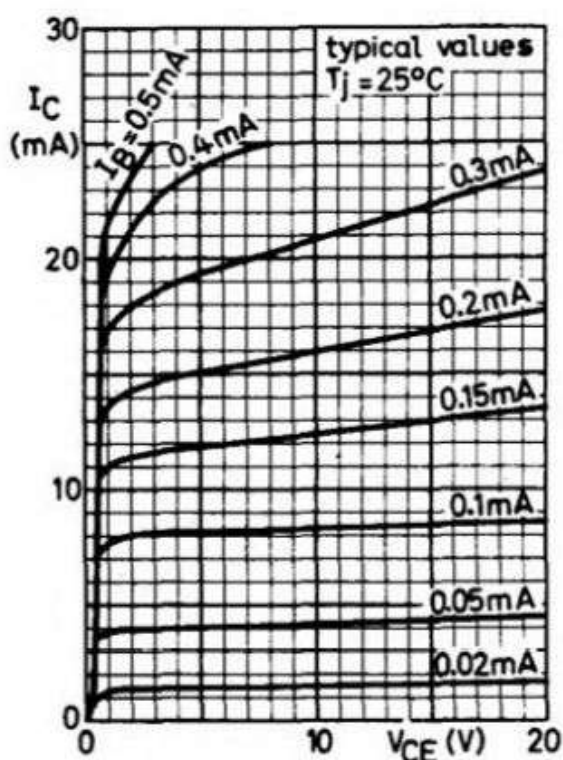
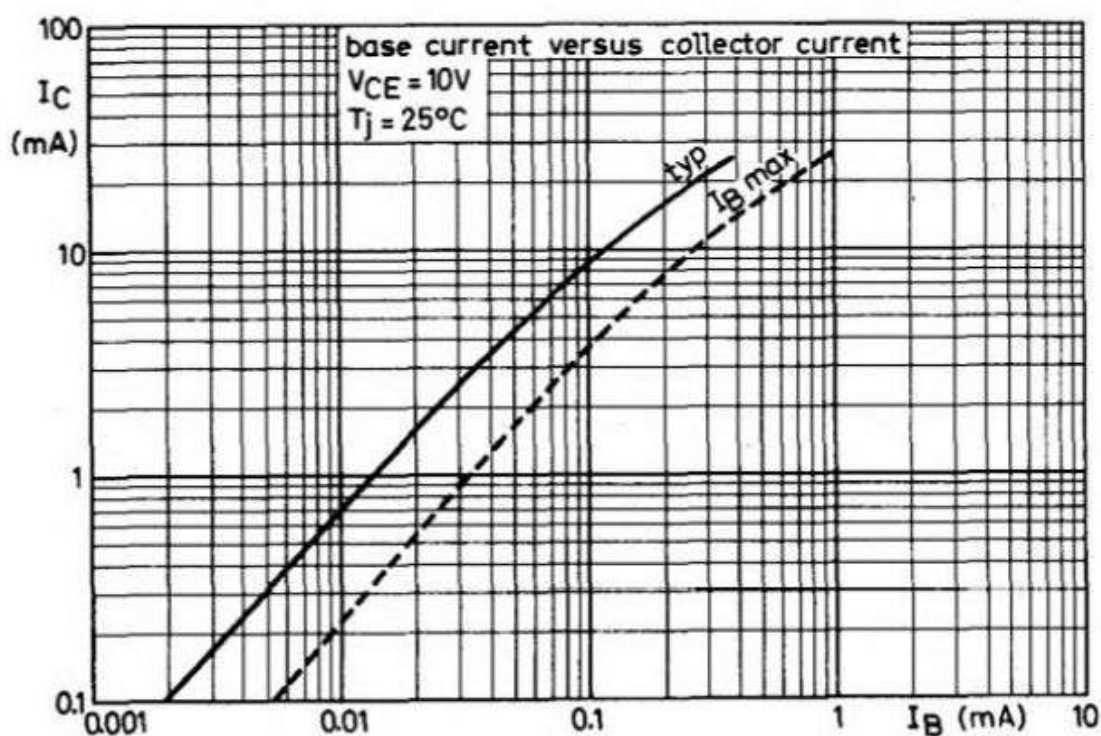
Maximum thickness of
printed board = 1.1 mm
Hole diameter = 0.77 to 0.83 mm

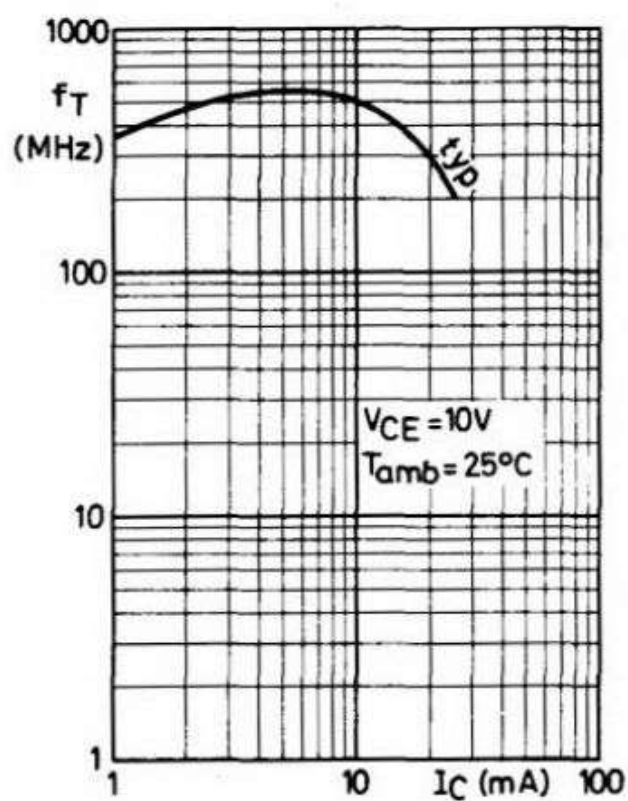
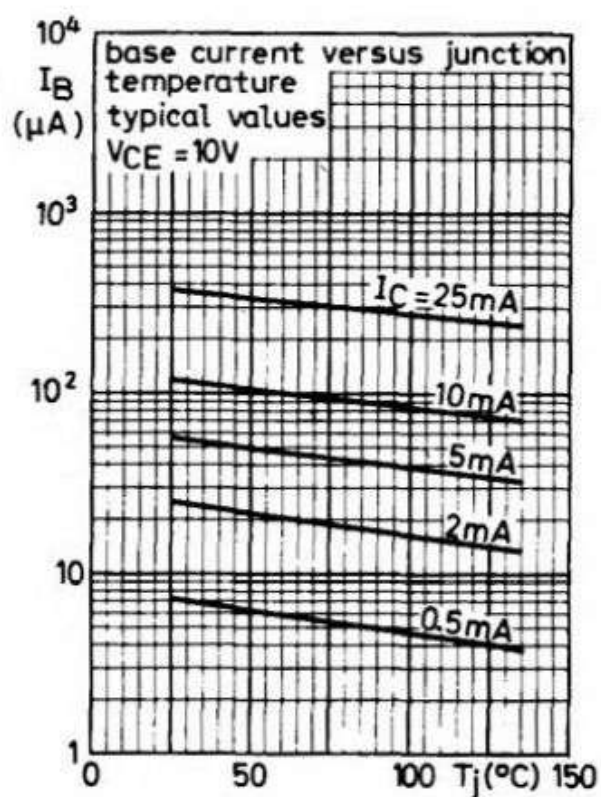
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See also General Explanatory Notes, Section IV

Mullard

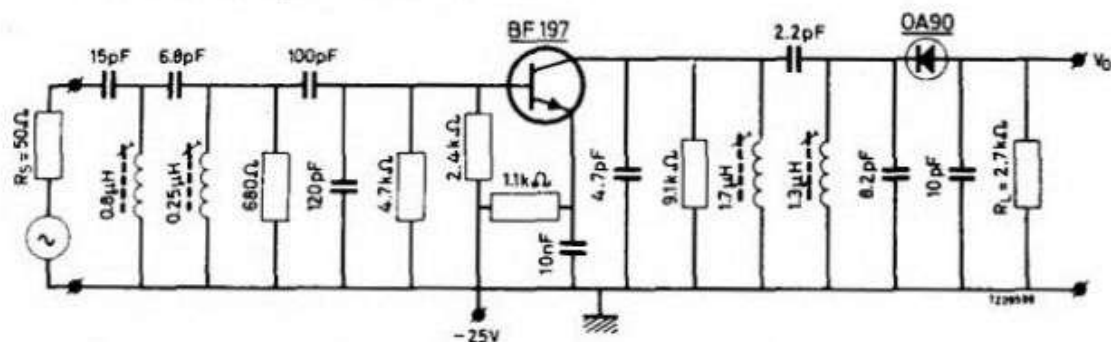






APPLICATION INFORMATION

Output stage of a television video i.f. amplifier with the BF197 transistor followed by a video detector circuit.



† V_O Video detector output voltage

$I_C = 7.2\text{mA}$, $V_{CE} = 16.6\text{V}$, $f = 38.9\text{MHz}$ *Min.* 6.0 *Typ.* 7.7 V

G_{tr} Transducer gain

$I_C = 7.2\text{mA}$, $V_{CE} = 16.6\text{V}$, $f = 36.4\text{MHz}$ — 25.5 dB

$$G_{tr} \text{ (in dB)} = 10 \log \frac{\text{output power in load } R_L}{\text{available power from source with } R_S}$$

Tuning frequency for all tuned circuits is 37MHz

†The output voltage V_O is defined as the voltage across the $2.7\text{k}\Omega$ detector load R_L for 30% synchronisation pulse compression.

