



AY-5-1224A

ELECTRICAL CHARACTERISTICS

Maximum Ratings*

Voltage on any pin with respect to V_{SS} +0.3 to -20V
 Operating Temperature Range 0° to +70°C
 Storage Temperature Range -65°C to +150°C
 Power Dissipation at 70°C Ambient—Total 500mW
 Per Output 50mW

*Exceeding these ratings could cause permanent damage. Functional operation of this device at these conditions is not implied—operating ranges are specified below.

Standard Conditions (unless otherwise noted)

$V_{SS} = 0V$
 $V_{GG} = -12$ to $-18V$
 Operating Temperature (T_A) = 0°C to +70°C

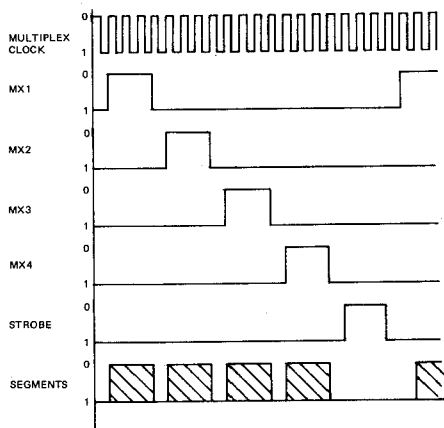
Characteristic	Min	Typ**	Max	Units	Conditions
Clock input frequency	DC	50/60	—	Hz	—
Clock input logic '0'	+0.5	—	-2	Volts	Note 1
Clock input logic '1'	-8	—	V_{DD}	Volts	—
Multiplex Clock Frequency	DC	—	50	KHz	Note 2
Interdigit Blanking	—	150	—	μS	at 6.67 KHz Note 3
Control inputs logic '0'	+0.3	—	-1.5	Volts	—
Control inputs logic '1'	-6	—	V_{DD}	Volts	—
Outputs Logic '0'	—	—	500	Ohms	$V_{OUT} = -2V$ $I_{OUT} = 4mA$
Outputs Logic '1' (Leakage)	—	—	10	μA	$V_{OUT} = -18V$
Supply Current	—	—	10	mA	$V_{GG} = -15V$

**Typical values are at +25°C and nominal voltages.

NOTES:

- The clock input pin may be taken position with respect to V_{SS} provided that the current is limited to 100 μA . The input will behave like a forward biased silicon diode in this condition.
- The frequency is determined by an external capacitor.
- At 6.67KHz multiplex frequency the digit ON time is 450 μS and the OFF time is 150 μS .

TIMING DIAGRAMS



AY-5-1224A WITH 0.3" LED DISPLAY

