

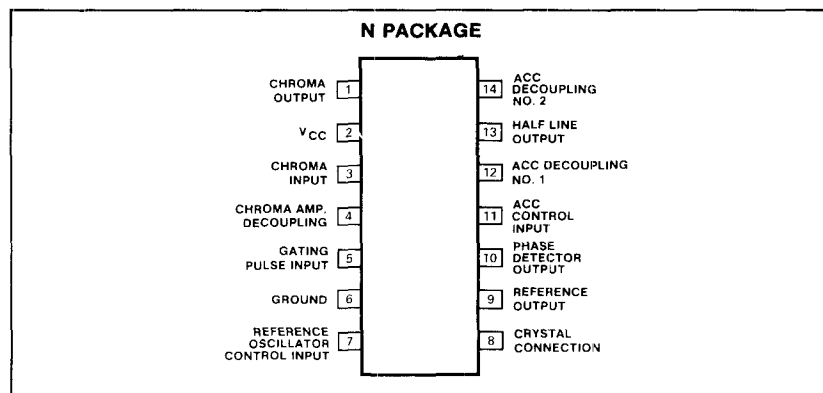
**DESCRIPTION**

Chrominance combination circuit for use in PAL television receivers.

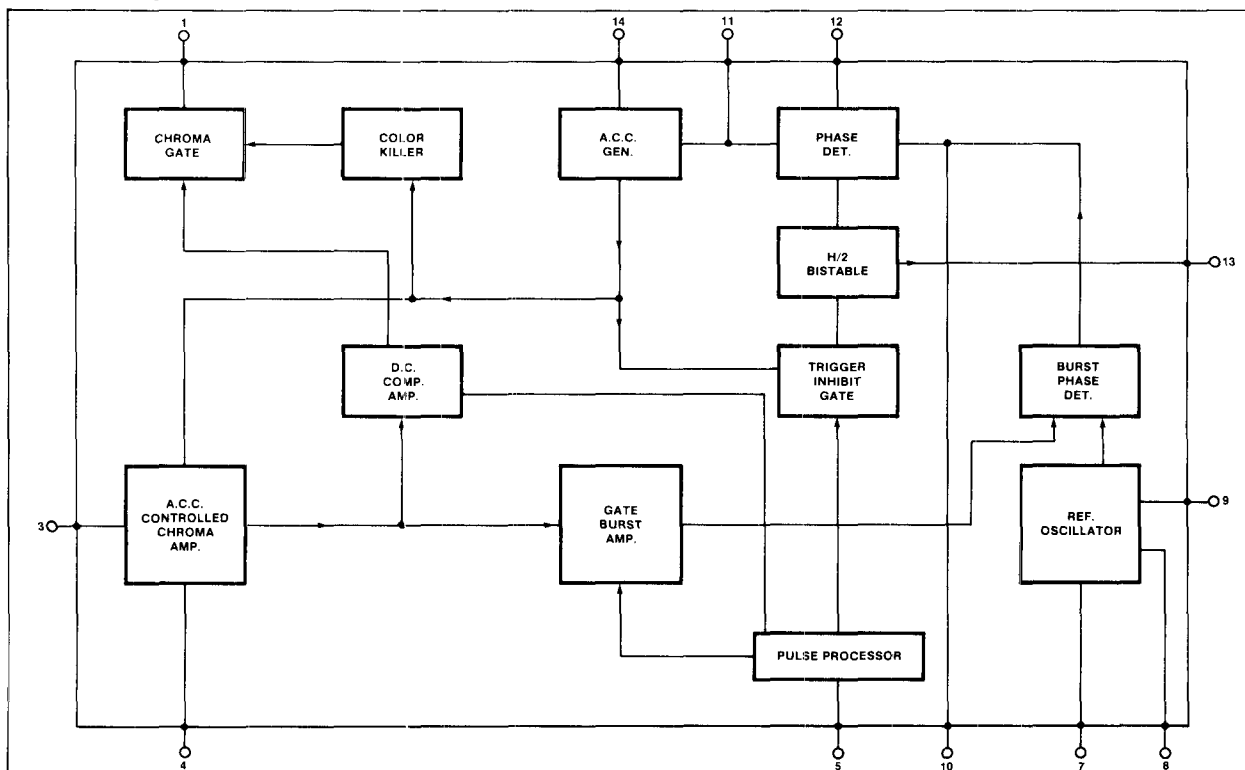
**FEATURES**

- Internal supply line stabilization
- 20dB ACC range—14dB + 6dB
- Low external component count
- Designed to be used in conjunction with TBA396 and TBA327/MC1327

**PIN CONFIGURATION**



**SYSTEM BLOCK DIAGRAM**



**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	RATING	UNIT
Power supply current	60	mA
dc current capability of reference output	4.0	mA
Chrominance input voltage	1.2	Vp-p
Operating temperature range	0 to +70	°C
Power dissipation (package limitation)	625	mW
Derate above T <sub>A</sub> = +25° C	5.0	mW/°C
Storage temperature range	-65 to +150	°C





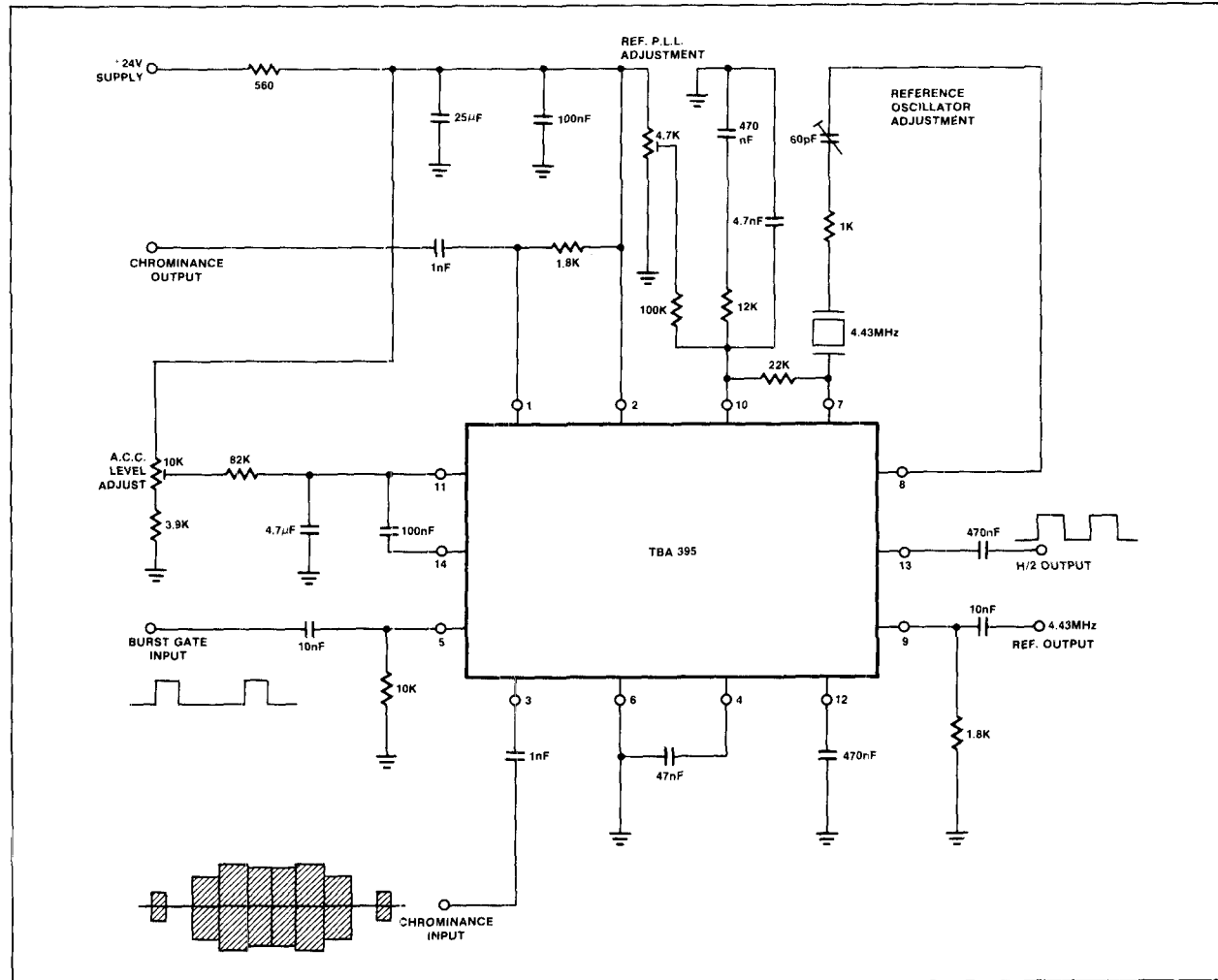
**DC ELECTRICAL CHARACTERISTICS**  $T_A = +25^\circ\text{C}$  unless otherwise specified.

PARAMETER	TEST CONDITIONS	TBA395			UNIT
		Min	Typ	Max	
Supply voltage		7.5	8.4	9.5	Vdc
Burst gate operating voltage		2.0		5.0	V
Chrominance output dc current	Color killer operating Color killer off	200		4.0	$\mu\text{A}$ $\mu\text{A}$

**AC ELECTRICAL CHARACTERISTICS**  $T_A = +25^\circ\text{C}$  unless otherwise specified.

PARAMETER	TEST CONDITIONS	LIMITS			UNIT
		Min	Typ	Max	
Forward transconductance	Chrominance output load = $560\Omega$ $f_N = 4.43\text{MHz}$	6.4			mmho
Chrominance input resistance		2.4	3.1	4.3	$k\Omega$
Reference oscillator pull-in range		$\pm 250$			Hz
Reference output		400	700		mV
H/2 bistable output		1.3	1.6	2.2	Vp-p

## TYPICAL CIRCUIT CONFIGURATION



## SETTING-UP NOTES

For subcarrier oscillator adjustment the chrominance input must be bypassed to ground via a 1nF capacitor. The ACC potentiometer is then set to 1.2 volts below pin 2 voltage using a high input impedance oscilloscope or Voltmeter ( $> 10m\Omega$ ). While the adjustment is made burst gate pulses must be applied to pin 5.

The oscillator free-running frequency can then be adjusted to sub-carrier value  $\pm 10Hz$ .

The loop will lock if a chrominance signal is re-connected.

With a peak to peak signal of 250mV (100% bars) the output on pin 1 should be adjusted to 400mV peak to peak using the ACC control potentiometer.

## APPLICATION NOTES

1. Normal decoupling precautions must be taken. For example pin 2 (8.4 volt circuit supply point) must be decoupled closely to

pin 6 (ground) thus preventing sub-carrier components leaking into sensitive areas of the circuit.

2. To prevent the radiation of sub-carrier harmonics, the connection from pin 9 (reference output) and pin 8 (crystal feedback) must be kept as short as possible.

3. The connection from pin 1 (chroma output) should be also as short as possible to prevent capacitive loading of the 1.8k $\Omega$  output resistor.